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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Edwin Kan et al.

Title: CHEMORECEPTIVE SEMICONDUCTOR STRUCTURE

Docket No.: 1153.078US1

Serial No.: 10/695,432

Filed: October 28, 2003

Due Date: March 3, 2005

Examiner: Allan Wilson

Group Art Unit: 2815

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- A return postcard.
 Response to Restriction Requirement (3 Pages).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: 
Atty: Bradley A. Forrest
Reg. No. 30,837

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 2nd day of March, 2005.

Name

Dawn M. Poole

Dawn M. Poole
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)

S/N 10/695,432

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Edwin Kan et al.	Examiner:	Unknown
Serial No.:	10/695,432	Group Art Unit:	2812
Filed:	October 28, 2003	Docket:	1153.078US1
Title:	CHEMORECEPTIVE SEMICONDUCTOR STRUCTURE		

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This is in response to the Restriction Requirement mailed December 3, 2004.

In the Restriction Requirement, the Examiner required that Applicants elect one of the following patentably distinct species of the claimed invention:

Species I. Figures 1 and 3, drawn to a floating gate having plurality of control gates, which appears to be claims 10-15 and 23.

Species II. Figure 2, drawn to a floating gate having plurality of control gates coupled to microfluidic fluid channels, which appears to be claims 16-18 and 25.

Species III. Figure 4, drawn to an array of chemoreceptive control gate transistors, which appears to be claims 5-9 and 19-21.

Species IV. Figure 5, drawn to a further exemplary floating gate transistor, which appears to be claims 1-4, 22 and 24.

-- Species are labeled incorrectly –Species below should be V and VI

Species VI. Figure 6, drawn to a CMOS chip.

Species VII. Figure 7, drawn to different width sensing gates.

[0001] Applicants provisionally elect without traverse Species IV. This species is believed to include at least claims 1-4, 22 and 24 as pointed out by the Examiner, but also at least claims 10-15 and 23, since FIG. 5 is appears to be merely a different type of view or representation of FIG. 1. The descriptions of the Figures are referenced here: